

DESIGN AND CONSTRUCTION OF INFRA – RED ALARM SYSTEM

BY

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(93/3476)

**A PROJECT SUBMITTED IN PARTIAL FULFILLMENT OF THE
REQUIREMENTS FOR THE AWARD OF BACHELOR OF
ENGINEERING (B.ENG) DEGREE.**

IN THE

**DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING,
SCHOOL OF ENGINEERING AND ENGINEERING TECHNOLOGY,
FEDERAL UNIVERSITY OF TECHNOLOGY, MINNA, NIGERIA**

MARCH, 2000.

CERTIFICATION

This is to certify that this project titled Design and construction of Infra-red Alarm system, was carried out by Abodunrin Olufunmilayo Samson under the supervision of Mr Paul Attah and submitted to Electrical and Computer Engineering Department, Federal University of Technology, Minna in partial fulfillment of the requirements for the award of Bachelor of Engineering (B.ENG) degree in Electrical and Computer Engineering.

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DECLARATION

I hereby declare that this project is the result of my own handwork and research which has never been presented by anybody. It was conducted under the supervision of Mr. Paul Attah in the Department of Electrical/Computer Engineering, School of Engineering and Engineering Technology, Federal University of Technology, Minna, Nigeria.

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SIGN & DATE

DEDICATION

This work is dedicated to the Almighty God and to my parents, Mr Gideon Olayinka Abodunrin and Mrs Esther Oyefunke Abodunrin who have seen to the fulfillment of my dream in life.

ACKNOWLEDGMENT

My thank goes to the Almighty God, for sparing my life till this present moment and for his grace given to me to complete this work successfully my profound gratitude goes to Mr. Paul Attah a dynamic, hardworking, Intelligent and God-fearing person, who has been my supervisor throughout the course of this project. I appreciate his expertise, patience, guidance and assistance throughout the period of supervising me on this project.

I also express my sincere gratitude to the Head of Electrical/Computer engineering Department of Federal University of Technology Minna,

Dr (ENGR) Y. A. Adediran.

My profound gratitude also goes to my officious aunt and her husband Mr. and Mrs. O.O. Adeyemo for their immense contributions, morally and Financially to the successful completion of this project. My appreciation also goes to Mr. Adebayo Ajibola for the practical way he had contributed to make this project a success.

I sincerely appreciate my colleague, Mr. Jimoh Olarinre for his relentless efforts for making this work a success. Also I appreciate the efforts of my friends, course mates and also beloved ones, who have been a channel of blessing to me.

I would like to appreciate the words of encouragement from my brothers and sisters which has been a fire power in achieving a laudable success in this project.

Finally, I give my profound gratitude to my lovely friend, Miss Bosede Omowumi Ogunsola who stood by me throughout the period of my course.

LIST OF ABBREVIATION/SYMBOLS

V_{rms}/I_{rms}	Instantaneous values of voltage/current
V_m/I_m	Maximum voltage/current
V_{dc}/I_{dc}	Dc output voltage/current
V_{cc}	Based voltage supply
V_{be}	Base-emitter voltage
V_{in}/V_{out}	Input/output voltage
B_{dv}	Breakdown voltage
PIV	peak - inverse voltage
Δv	peak - to - peak ripple voltage
I_l/I_d	Load current/diode current
$I_c(sat)$	saturation collector current
h_{FE}	D.C Current gain
$h_{FE}(sat)$	Forward, current, transfer ratio at saturation
P_o	Power output
R_l	Load resistor
C	Capacitor
f	Frequency
γ	Overdrive factor
μF	Micro farad
NC/NO	Normally closed/Normally opened
LED	Light Emitting Diode
T_1	Transformer 1
IC	Integrated circuit
T_r	Transistor
UJT	Unjunction transistor
SPKR	Speaker

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CHAPTER ONE

GENERAL INTRODUCTION

1.1 INTRODUCTION

As this time it is possible that your home or business is under surveillance by professional burglar and date is set for the job. This could take place in the dead of night on a weekend when you are away.

All the buglar must do is learn the daily habits of you and your family and wait until the ideal oppotunity occurs.

By taking the proper preventive method to reduce the chances of your property being burglarized is to install a good burglar alarm system and let it be known that such a system exists.

All alarm systems no matter how complex, might be broken down into four basic sections. The infrared pulse generator emitting the infrared radiation, sensors, control unit, power supply and the alarm indicator.

Some current applications of infrared system are:

MILITARY: Military fire control at night or during the day when vision isdiminished due to fog, smoke or haze. Detection and tracking of ships, aircraft, missiles, surface vehicles and personnel, submarine detection and range finding.

MEDICAL: Early detection and identification of cancer, obstacle detection for the blind, location of blockage in a vein and early diagnosis of incipient stroke.

SCIENTIFIC: Satellite and space communication. Environmental survey and control, detection of life and vegetation on other planets and measurement of lunar and planetary temperature.

INDUSTRIAL: Aircraft landing aid and traffic counting forest fire detection and natural resource detection.

1.2 AIMS AND OBJECTIVES

The aim of this project is to design and construct a multipurpose movement detector which will be able to form the basis of all sorts of burglar alarms and automatic controllers.

It will work either as a single interrupted beam alarm over substantial distances or will directly detect moving objects or persons by measuring changes in the level of reflected infrared over shorter distances. It is intended that the project will be insensitive to ordinary visible light.

1.3 METHODOLOGY

Each of the stages was designed separately (i.e. modular approach method,). This project was built using integrated circuits (I.C.) And discrete components.

The design is composed of four basic sections, the infrared pulse generator, sensors, control unit, power supply and the alarm indicator.

The circuit diagram of the transmitter is a standard 555 astable circuit having the values of the timing components (R_3 , R_4 , And C_3) chosen to give a suitable operating frequency. The output waveform of the 555 astable was connected to the emitter diode, connected between the output of the 555 astable was connected to the emitter diode, connected between the output of the IC, and the Positive supply rails

The next (fig. 1) is the circuit diagram of the receiver (detector). The detector diode (D_5) is coupled to receive transmitted signal filtered by R_5 and C_4 . At the receiver circuit is the amplifier which is a simple two – stage common emitter type capacitively coupled. The output of the amplifier was rectified and smoothed by D_6 , D_7 , C_9 and R_{12} . The Schmitt trigger is based on IC_2 . R_{14} was incorporated to provide hysteresis and to obtain desired sensitivity (a compromise between these two qualities being maintained)

The output of the comparator (IC_2) was coupled to the timer circuit. The timer circuit employed is a 555 timer IC which has a monostable operation. The timing components are R_{16} and C_{12} . Fig. 1. The relay is a 12V, 400 Ω relay (RLA_2). The making of the relay depends on the timer circuit.

The normally open, NO terminal of the relay feeds the alarm circuit. Two oscillator were used at the alarm circuit. The unijunction oscillator and the astable multivibrator. The output of the unijunction oscillator was coupled to the base of one of the transistor of the astable multivibrator. The output of the two oscillators was coupled to a speaker through an impedance matching transistor.

1.4 LITERATURE REVIEW

Sir William Herschel in 1800 discovered infrared radiation when he worked for the Royal Navy. He generated the radiation by vibration and rotation of the atom and molecules within some materials at temperature above absolute zero that is 0°k or -273 ;

Herschel referred to the new portion of the spectrum by such names as “invisible rays” “radiant heat”, “dark heat”, and “the rays that occasion heat”.

In the recent years, there has been an increasing emphasis on the research design, development and deployment on various infrared devices and systems for military application at night or during the day when vision is diminished by fog, haze, smoke or dust.

During World War 1 an infrared system could detect aircraft, at a distance of 1.6km and a person at a distance of 300m.

Many sensitive infrared detectors, such as photo-detector and image converters were developed during World War II. In the late 50's, the side winder and falcon heat - seeking infrared guided missiles were developed.

Furthermore, infrared techniques became applicable to the altitude stabilization of space vehicles, measurement for planetary temperature, earth mapping and early detection of cancer.

The fundamental work on infrared thermal imaging systems was contributed by many dedicated scientist and engineers, such as Hudson, Jones and Johnson.

1.5 PROJECT OUTLINE

The design is such that objects are detected as they pass between the emitter and sensor devices (fig:1). They are placed in short distance apart depending on the range are want to cover.

The emitted signal is the form of infrared pulses and the generator is an oscillator, which pulses at infrared LED.

The sensor unit consist of infrared detector diode feeding a very high gain amplifier.

The control unit read the condition of the sensor circuit and determine if current is flowing or not. The control unit detect the loss of current flow and produce an alarm output.

A simple alternating current power supply is used to power the inferred alarm system. A 12V battery can still be used as a stand by supply.

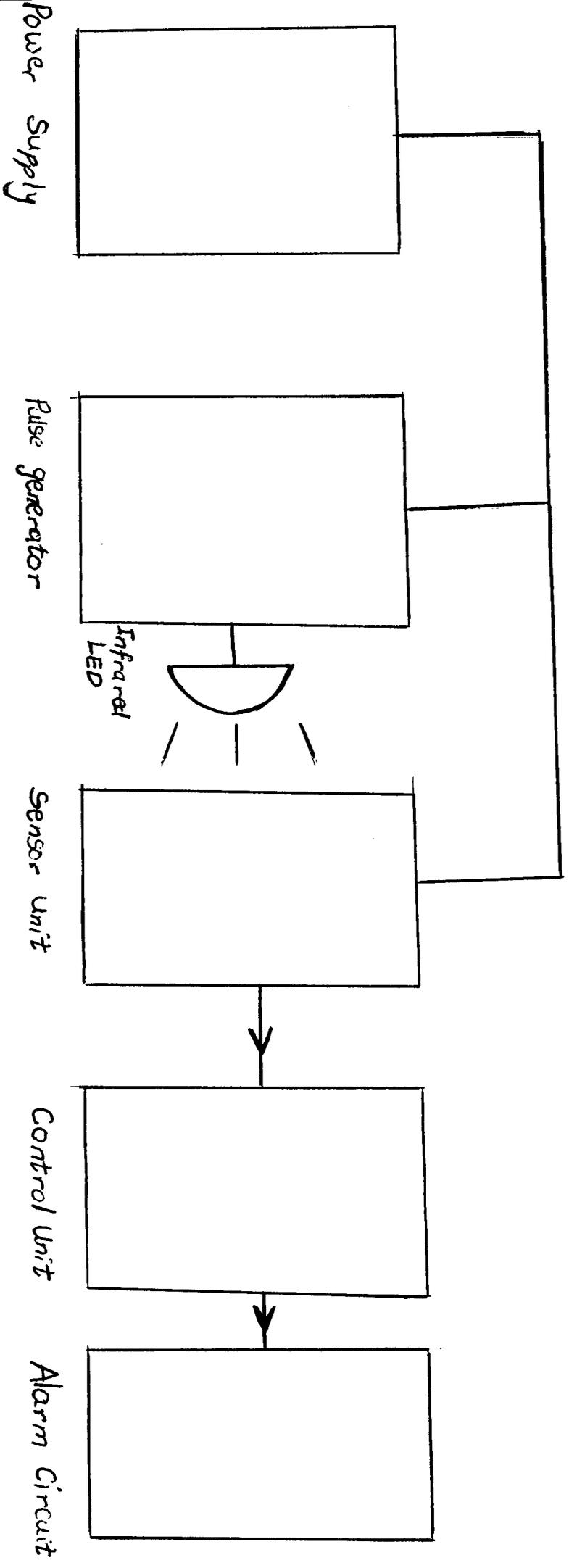


Fig. 1.1 Block diagram of Infrared alarm System

CHAPTER TWO

SYSTEM DESIGN

2.1 POWER SUPPLY

i. METHOD OF RECTIFIATION:

A full-wave rectifier network with center tap transformer was employed to:

- obtain a higher dc voltage output since

$$V_{dc} = 2V_m/\pi \quad \text{and} \quad I_{dc} = 2I_m/\pi$$

- Increase the peak inverse voltage (PIV).

PIV is the maximum voltage that can be applied in the reverse direction without breakdown (through the rectifying diodes)

$$PIV = 2V_m, \quad (PIV = V_m \text{ for half-wave})$$

ii Transformer selection

$$P_o = V_{dc} \times I_{dc}$$

$$= 2V_m/\pi \times 2I_m/\pi$$

$$= 2\sqrt{2} V_{rms} / \pi \times 2\sqrt{2} I_{rms} / \pi$$

$$= 2\sqrt{2} V_{rms} / \pi \times 2\sqrt{2} / \pi \times V_{rms} / R$$

$$= 8V_{rms}^2/\pi^2 R$$

$$\text{therefore } V_{rms} = \sqrt{P_o \pi^2 R / 8}$$

for this project,

P_o is 15W, R_l 8 Ω

$$V_{rms} = \sqrt{15 \times \pi^2 \times 8 / 8}$$

$$\approx 12.167V$$

$$= 12V$$

therefore a transformer of 240V : 12V was chosen

iii diodes rating

Voltage rating: the maximum voltage, which occurs across the diode in the reverse direction, peak inverse voltage (PIV) must be less than the breakdown voltage of the diode if it is not to conduct appreciably in the reverse direction.

for a full-wave,

$$PIV = 2 V_m$$

$$V_m = \sqrt{2} V_{rms}$$

$$= \sqrt{2} \times 12$$

$$= 16.97V$$

$$V_m \approx 17V$$

$$\text{Therefore } PIV = 2 \times 17$$

$$= 34V$$

the breakdown voltage must be greater than PIV ($Bdv > PIV$)

Therefore diodes of breakdown voltage of 50V (>34) are desirable.

Current rating:

$$\begin{aligned} I_{dc} &= 2I_m/\pi \\ &= 2\sqrt{2}/\pi \times V_{rms}/R \\ &= 2\sqrt{2} \times 12/8\pi \\ &= 1.35A \end{aligned}$$

I_{dc} = the mean load current

Therefore a diode of current rating of 1.5A was chosen.

From the E.C.G. data book IN5391 diode satisfy the requirements.

iv. CAPACITOR SELECTION

voltage rating:

capacitor voltage, V_c rating $> \sqrt{2} V_{rms}$

$$\sqrt{2} \times 12 = 16.97V (\sqrt{2} V_{rms})$$

$$V_c \geq \sqrt{2} \times 2 V_{rms}$$

$$V_c \geq 16.97V$$

Therefore a capacitor of voltage rating of 25V was chosen.

Capacitance rating:

$$\Delta V \approx V_m/2fRC$$

$$\Delta V \propto 1/C$$

If a peak – to – peak ripple voltage of not more than 10V is to be tolerated

$$10 = 12\sqrt{2}/(2 \times 50 \times 8 \times C)$$

$$C = 12\sqrt{2}/8000$$

$$= 2121 \mu F$$

therefore a capacitor of 2200 μ F capacitance was chosen

v **ZENER DIODE AND LIMITING RESISTOR, RS**

a varying load: fixed supply voltage

$$R_s = (V_{in} - V_{out}) / (I_{dmax} + I_{lmin}) \dots\dots\dots(1)$$

$$R_s = (V_{in} - V_{out}) / (I_{dmin} + I_{lmax}) \dots\dots\dots(2)$$

b. varying supply voltage, fixed load

$$R_s = (V_{in\ min} - V_{out}) / (I_{dmin} - I_L) \dots\dots\dots(3)$$

$$R_s = (V_{in\ max} - V_{out}) / (I_{dmax} + I_L) \dots\dots\dots(4)$$

Using equation (1) above,

$$V_{in} = 14.7V$$

$$V_{out} = 12V$$

$$I_d = 6\ mA$$

$$\begin{aligned} \text{Therefore } R_s &= (14.7 - 12) / 0.006 \\ &= 450\Omega \end{aligned}$$

the preferred value of $R_s = 470\Omega$

vi **Protective fuse**

current rating: current rating of fuse should be higher than the mean load

current (I_{dc})

$$\begin{aligned} I_{dc} &= 0.63I_m \\ &= 1.35 \end{aligned}$$

therefore a fuse of 1.5A was used. (See fig.2/)

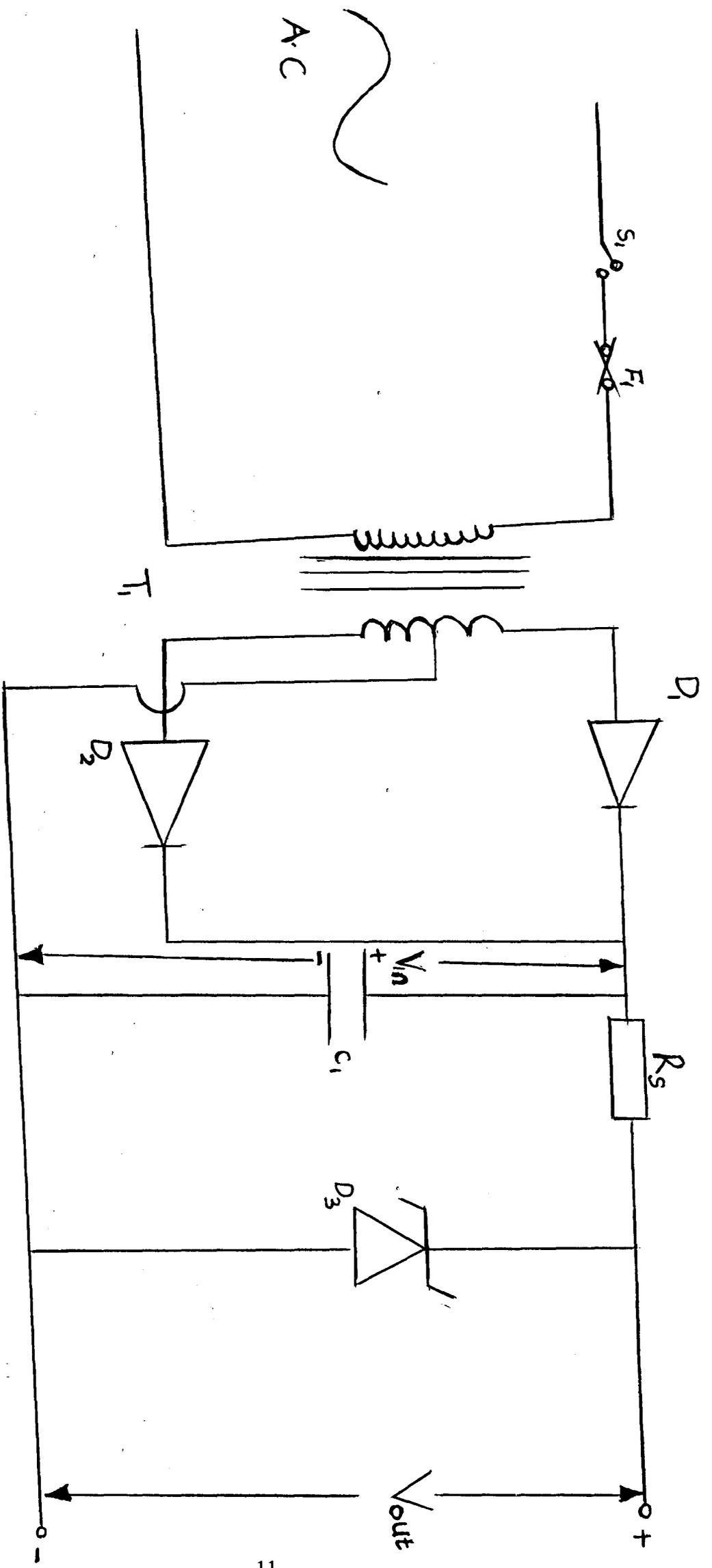


Fig 21
Power Supply Circuit

2.2 THE PULSE GENERATOR STAGE

The pulse generator is figure 2.2(a), a standard 555 astable circuit having the values of the timing components (R_3 , R_4 , and C_4) chosen to give a suitable operating frequency.

The frequency, f , of the pulses produced by the astable is given by

$$f = 1 / (t_1 + t_2) \text{ HZ.}$$

With the mark period three times longer than the space timer so that the emitter diode is only pulsed on for 25% of the time figure 2.2(b). This was done to keep the average current consumption of the circuit down to a reasonable level. Figure 2.2(a) and 2.2(b)

From the above:

$$\text{LED on} = R_4 C_3$$

$$\text{LED off} = (R_3 + R_4) C_3$$

$$\text{LED off} : \text{LED on} = 3:1$$

$$(R_3 + R_4) C_3 : R_4 C_3 = 3:1$$

$$(R_3 + R_4) / R_4 = 3/1$$

$$R_3 = 2R_4$$

$$\text{From } f = 1 / (t_1 + t_2) \text{ Hz}$$

$$\text{And } t_1 = R_4 C_3, t_2 = (R_3 + R_4) C_3$$

$$f = 1 / (R_4 C_3 + (R_3 + R_4) C_3) \text{ Hz}$$

$$= 1 / (R_3 + 2R_4) C_3 \text{ Hz}$$

$$\text{but } R_3 = 2R_4$$

$$\text{therefore } f = 1 / \{2R_4 + 2R_4\} C_3$$

$$= 1 / (4R_4 C_2) \text{ Hz}$$

$$f \propto 1/R4 \text{ (with } 1/4C3 \text{ constant)}$$

with a frequency of 10KHz and a capacitance of 10n F

$$10 \times 10^3 = 1/(4R4 \times 10 \times 10^{-9})$$

$$\text{therefore } R4 = 2.5K$$

The preferred value of R4 is 10K (for maximum adjustment)

$$\text{Since } R3 = 2R4,$$

$$R3 = 2(2.5) K$$

$$= 5K$$

preferred valued of R3 is 20K .

D4 is an LED (TIL 38) with the following parameters:

$$\text{Maximum forward voltage, } V_F = 1.5V$$

$$\text{Dc forward current, } I_F = 150mA$$

$$\text{Therefore } R2 = (V_{cc} - V_F)/I_F$$

$$= (12 - 1.5)/150 \times 10^{-3}$$

$$= 70 \Omega$$

the preferred value is 56 Ω

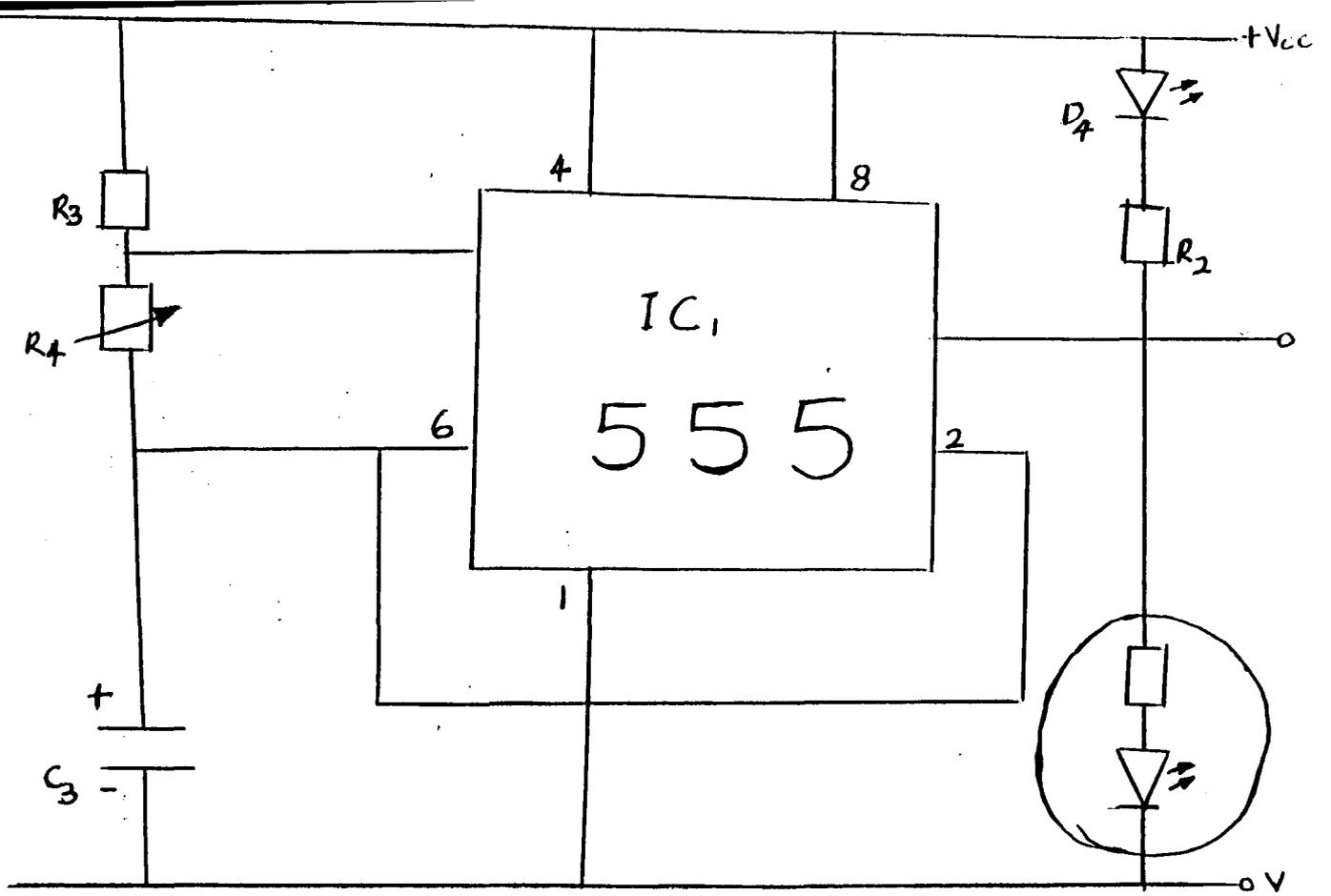


Fig 2.2(a) The Pulse generator Circuit

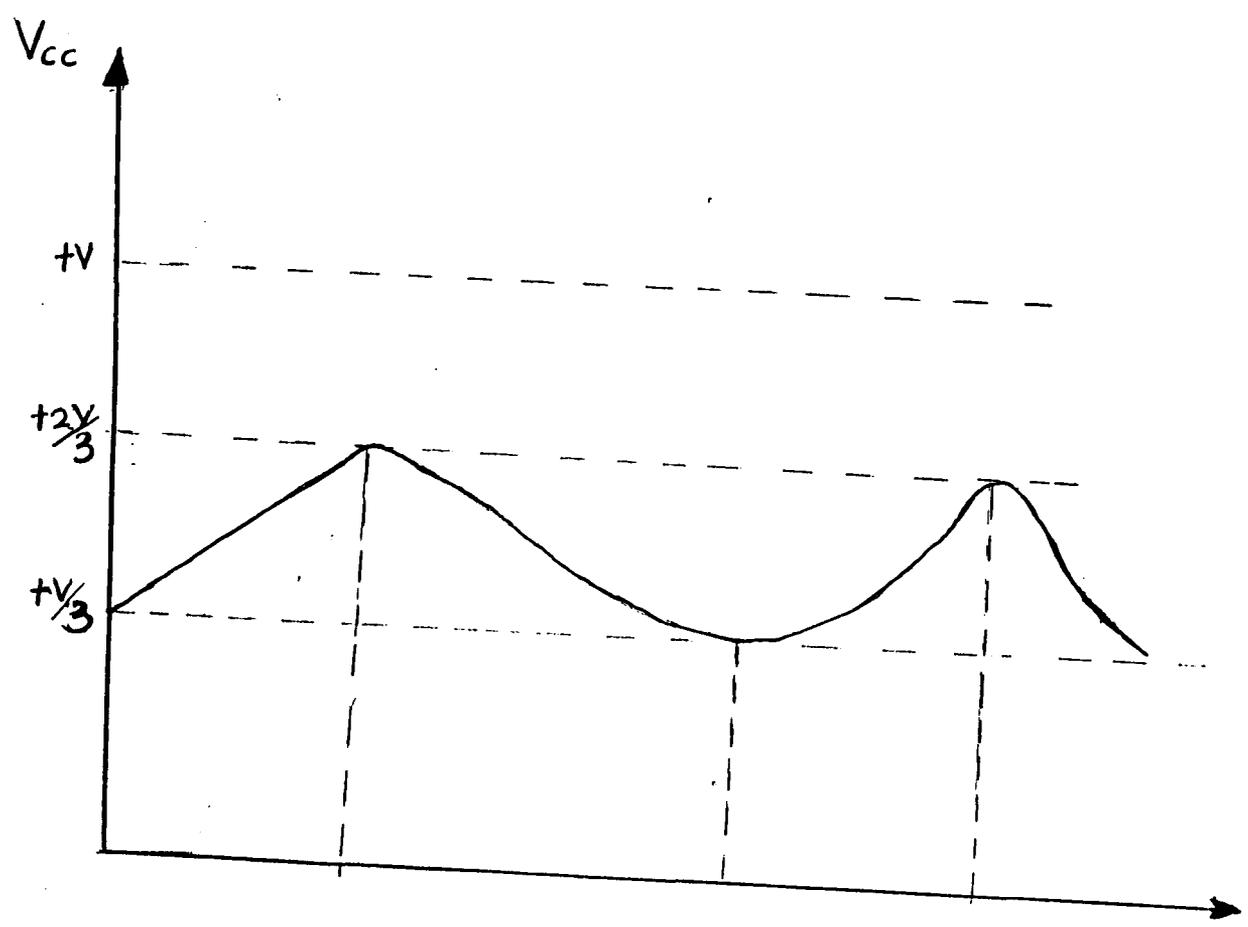


Fig 2.2(b) 14

2.3 AMPLIFIER STAGE

For the transistor to be used as amplifier, the following were done.

- Bias at the mid-point of transistor operation
- Collector resistor, R_c and base resistor, R_b were included
- A transistor of high gain, h_{FE} were chosen
- Base resistor value used was very high

Since high input resistance implies small base current and this together with high h_{FE} increases the sensitivity of the transistor figure 2.3

Capacitor C_5 was included to block d.c (component of the signal into the transistor).

A voltage – shunt feedback technique was used. That is voltage (as output) fed back as current. R_6 (R_7 & R_9) provided base – current bias for the two transistors. They also act as feedback elements figure 1.

Refer to figure 5

To bias to the center of the dc load line, that is, for the output voltage at the collector to be $V_{cc}/2$,

$$V_{cc}/2 = I_c R_c$$

$$V_{cc} = 2I_c R_c \dots\dots\dots(i)$$

Loop equation gives $V_{cc}/2 = I_b R_b + V_{be}$

Neglecting V_{be} , we have $V_{cc} = 2I_b R_b$ in $\dots\dots\dots(ii)$

Equating (i) and (ii)

$$2I_c R_c = 2I_b R_b$$

$$R_b = I_c R_c / I_b$$

But $i_c/i_b = h_{FE}$

Therefore $R_b \approx h_{FE} R_c$

From $2i_c R_c = V_{cc}$,

$$R_c = V_{cc}/2i_c$$

for Tr1 and T2 (BC109C);

$I_c \text{ max} = 0.1 \text{ amps}$

$h_{FE} = 400$

$$R_c = 12/2(0.1)$$

$$= 60\Omega$$

the preferred value of R_c is 68Ω

power rating of R_c , P_{Rc}

$$P_{Rc} = I^2 R_c$$

$$= (0.1)^2 \times 68$$

$$= 0.68W$$

$$\approx 1W$$

therefore the preferred value of R_c is 68Ω , $1W$

from $R_b \approx h_{FE} R_c$

$$R_b = 400 \times 68$$

$$= 27200\Omega$$

The preferred value of $R_b = 30K$

Power rating = $1/4W$

Therefore the value of $R_b = 30K$, $1/4W$

The value of the capacitor, C5

$$X_c \ll r_{in}$$

X_c = reactance of the capacitor

r_{in} = input resistance

$$\text{If } X_c = 10\% r_{in}$$

$$X_c = 1/2\pi fC$$

$$1/2\pi fC = 10/100r_{in}$$

From the circuit: fig. 11

$$V_{cc} = I_b (R_c + R_b) + I_c R_c + V_{be}$$

$$r_{in} = R_c + R_b$$

$$R_b \gg R_c$$

$$r_{in} \approx R_b$$

$$\text{Therefore } r_{in} \approx 30K$$

$$1/2\pi fC = 10/(100 \times 30,000)$$

$$C = 1/(100\pi \times 3000) \quad \{ f = 50\text{Hz}, \quad \pi = 3.142 \}$$

$$= 1.06 \times 10^{-6}$$

$$\text{The preferred value of } C5 = 1 \mu\text{F}$$

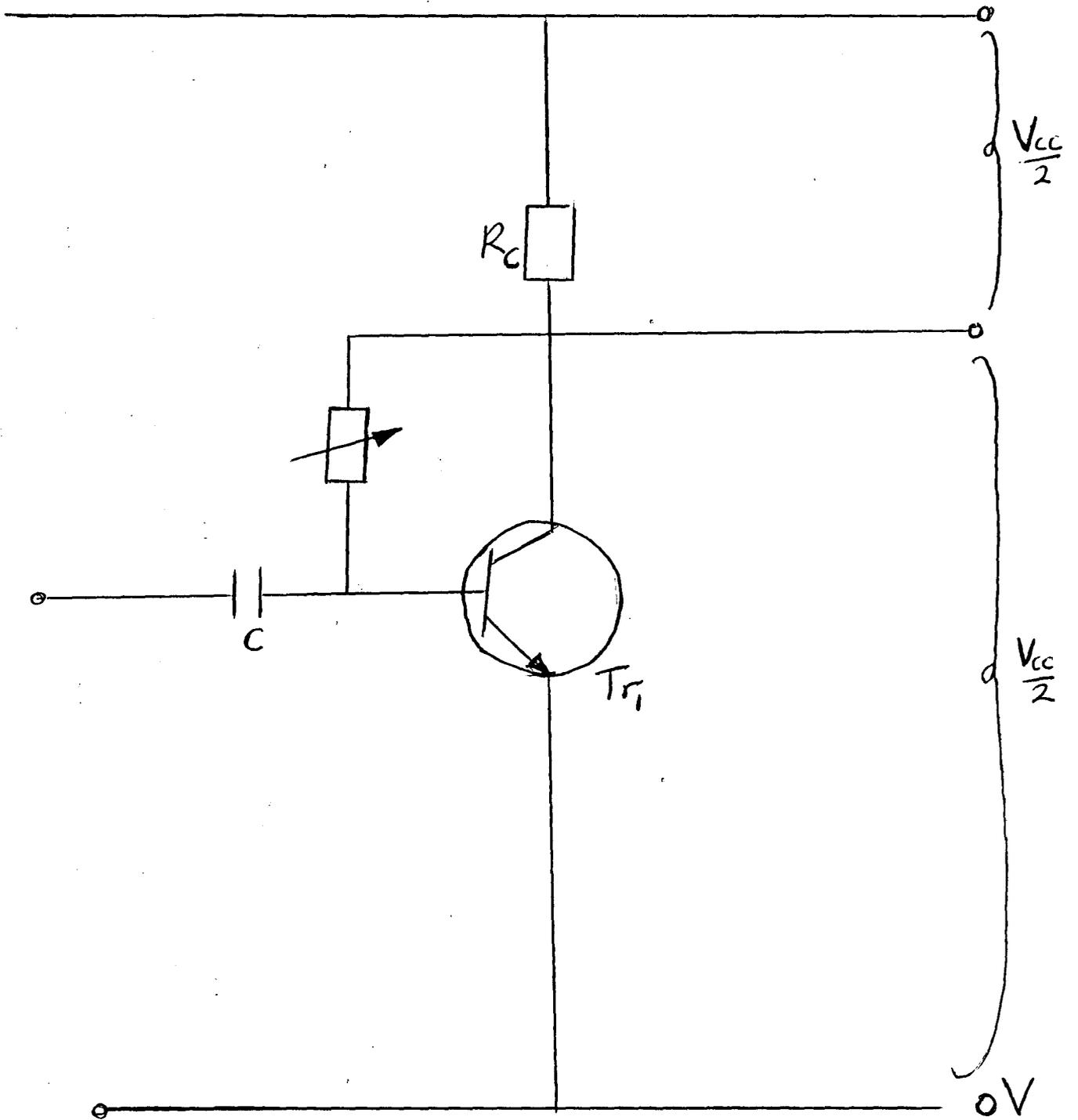


Fig 2.3 Biasing a Transistor at the Q-point

2.4 THE SCHMITT TRIGGER (COMPARATOR)

The Schmitt trigger is a circuit that will produce sharp rectangular pulses. It is used to know which of two signals is larger, or to know when a given signal exceeds a pre-determined value. It may also be used in wave shaping applications. For example, various periodic waveforms such as sinusoid and triangular waveforms may be converted to square wave or pulse trains.

Schmitt trigger makes use of positive feedback. The effect of the feedback resistor in the circuit is to have two thresholds which eliminates multiple triggering. Also, the positive feedback ensures a rapid output transition regardless of the speed of the input waveform. The input of a Schmitt trigger depends on both the effect voltage and on its recent history.

This effect is called hysteresis fig. 2-5.

Comparators are examples of non-linear circuits. The two op-amp input voltages may have completely different values because of the absence of negative feedback.

The divider network consisting of R1 and R2 stabilizes a voltage at the non-inverting input terminal proportional to the output voltage. The magnitude of the voltage across R2 in Fig 2-4 will be defined as threshold (triggering) voltage, V_T

$$V_T = R_2 V(\text{sat}) / (R_1 + R_2)$$

By choosing an appropriate value of V_T , one can minimize the effect of noise at the transition points.

The transition points corresponds to the input voltage becoming more positive

than V_T in one direction and becoming more negative than $-V_T$ in the opposite direction.

If the output lies at the positive saturation limit V_{pos} , V_T will be positive and equal to

$$V_+ = R_2/(R_1 + R_2) V_{pos}$$

Applying a V_{in} greater than $R_2/(R_1 + R_2) V_{pos}$ to the negative terminal causes $(V_+, -V_-)$ to become negative, in turn forcing V_{out} to its negative saturation limit of V_{neg} . If V_{in} is returned to Zero, however, V_{out} will remain negative at V_{neg} eg, with

$$V_+ = R_2/(R_1 + R_2) V_{neg}$$

Now, consider fig 8a

With $V_2 < V_1$, $V_0 = +V_0$

$$V_1 = V_0 - R_2/(R_1 + R_2) V_{cc} \dots\dots\dots(1)$$

With $V_2 > V_1$, $V_0 = -V_0$

$$V_2 = V_0 + R_2/(R_1 + R_2) V_{cc} \dots\dots\dots(ii)$$

The width of the hysteresis band $V_2 - V_1$ (V_H)

Is easily controlled by adjusting R_2

$$V_H = V_2 - V_1 = V_0 + R_2 V_{cc}/(R_1 + R_2) - \{ (V_0 - R_2 V_{cc}/(R_1 + R_2)) \}$$

Therefore $V_H = 2R_2 V_{cc}/(R_1 + R_2)$

For a symmetrical square wave:

$$\begin{aligned} V_1 &= -V_2 \\ &= - R_2/(R_1 + R_2) V_{cc} \end{aligned}$$

Design Procedure

Consider fig 2.5

1. R^* (variable resistor) was used as a resistive divider to put the threshold at

approximately the right voltage. Figure 2.5

2. The (positive) feedback resistor R was chosen to produce the required hysteresis figure 2.6(b)

Now with the power supply voltage at V_{pos}

(ie + 12V = V_{sat})

$$V_{in} = R_2/(R_1 + R_2) V_{pos}$$

For a faster transition, the reference voltage (or threshold) must be as small as possible.

Thus reference voltage of 0.5V was chosen.

$$V_T = R_2/(R_1 + R_2) V_{pos}$$

$$0.5 = R_2/(R_1 + R_2) \times 12$$

$$R_2/(R_1 + R_2) = 0.5/12$$

$$\text{At } R_1 = 1M$$

$$R_2/(1000 + R_2) = 0.0416$$

$$R_2 = 41.67K$$

The preferred value of $R_2 = 47K\Omega$

($R_2 \equiv R_{13}$ in the complete circuit diagram) figure 1 .

The schmitt trigger has a bistable characteristic. In other words it has two stable states. These states are the positive saturated state whereby the output drives (V_+ - V_-) in the positive direction and when V_{out} and (V_+ - V_-) are both negative.

Where (V_+ - V_-) are power supplies.

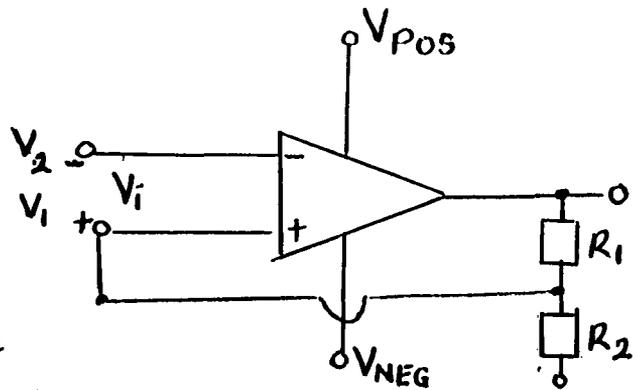


Fig 2.4

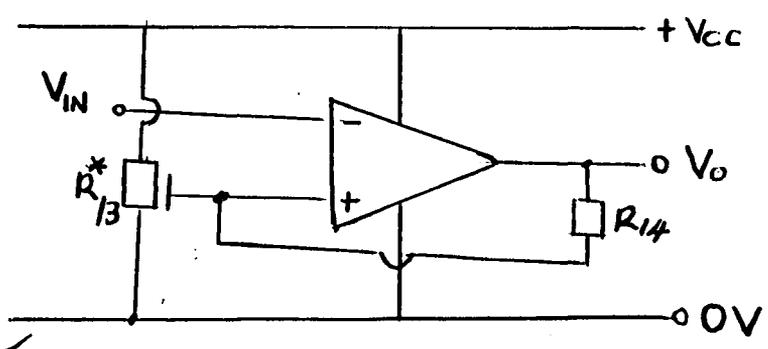


Fig 2.5

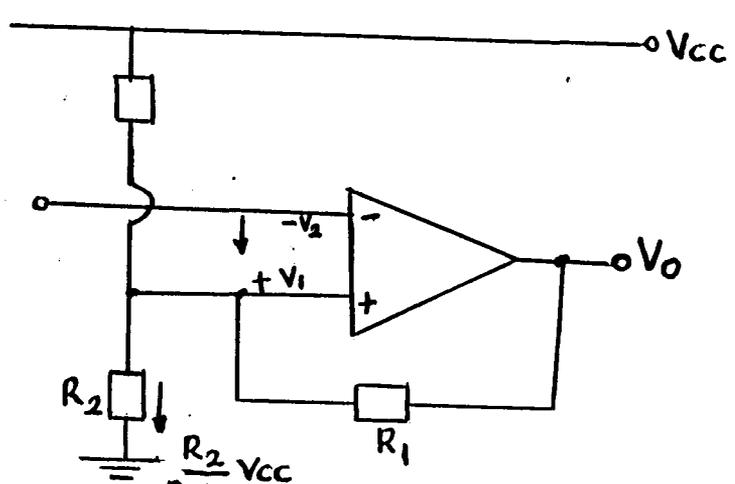


Fig 2.6(a) Schmitt trigger circuits

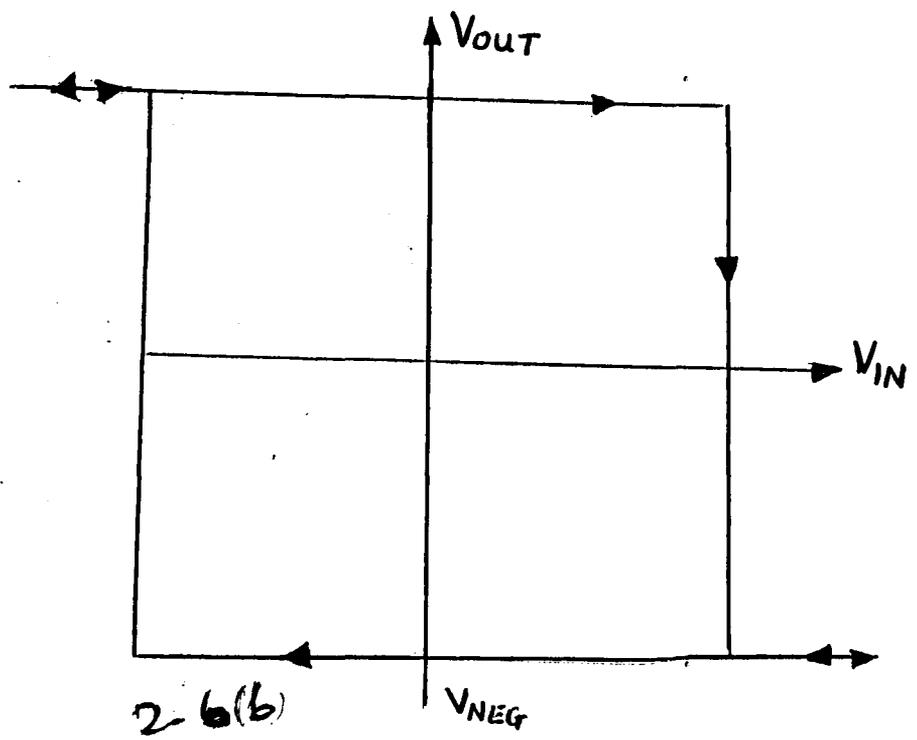


Fig 2.6(b) Plot of V_{out} Versus V_{in} for the Schmitt trigger

2.5(A) TIMER CIRCUIT

The 555 timer circuit is designed to switch on a device for a pre – set period of time and then switch it off..

A signal through pin 2 of the 555 timer makes pin 3 which was at 0 volts goes to $+V_{cc}$ and the relay energizes. The relay then remains on for the period determined by the timing components R_{16} and C_{12} . Figure 9a.

It then turns off that is pin 3 goes back to 0 volts. The circuit of this type has a monostable operation. Fig 9(b).

Delay time, t is given by

$$t = 1.1 RC \text{ s}$$

$$t \propto R \text{ (1.1C = constant)}$$

With $t = 5 \text{ s}$, and $C = 10 \mu \text{ F}$

$$5 = 1.1 \times 10 \times 10^{-6} R$$

$$R = 454.5 \text{ K}$$

The preferred value of $R_{16} = 470 \text{ K}$

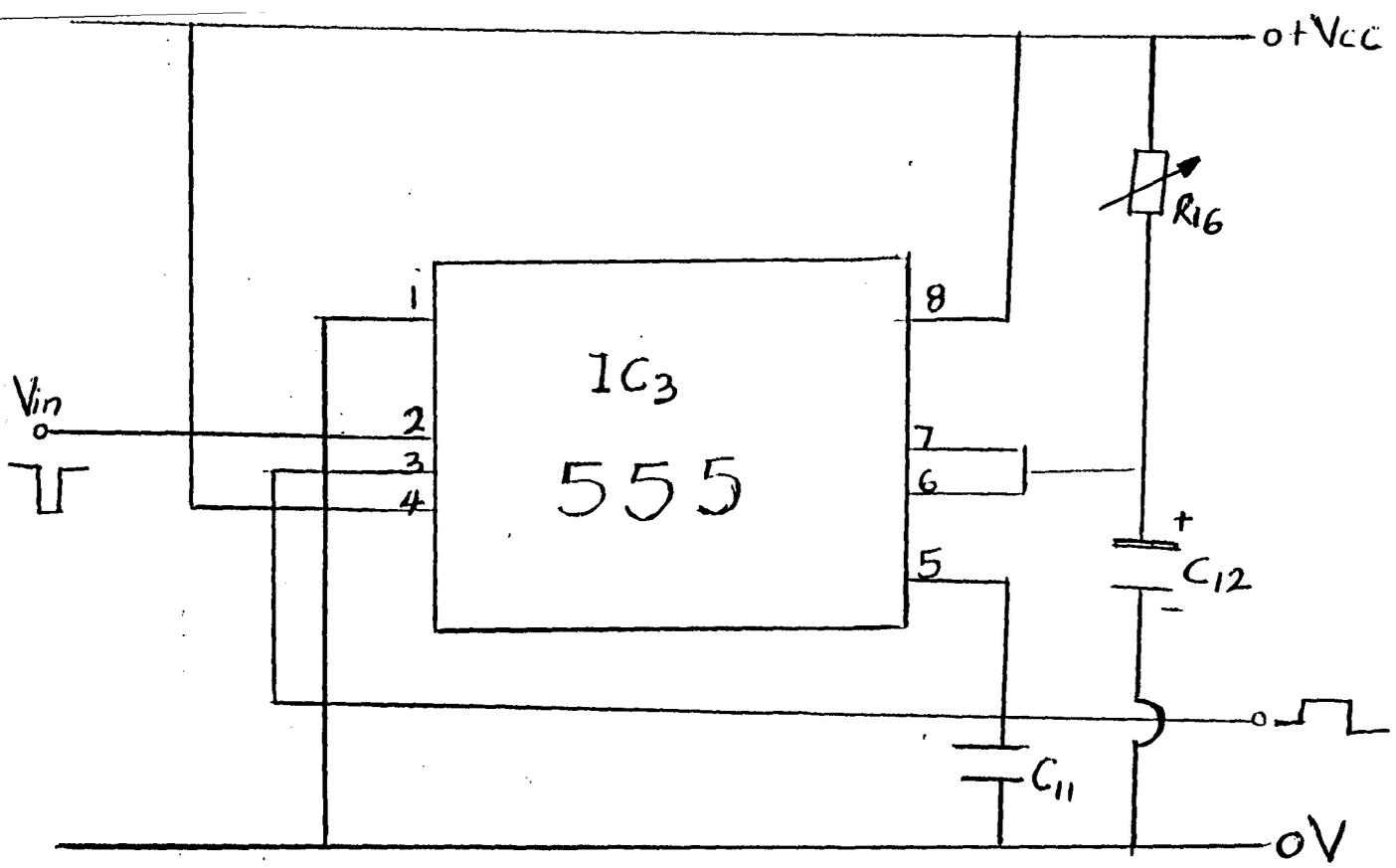


Fig. 2.7(a) Timer circuit

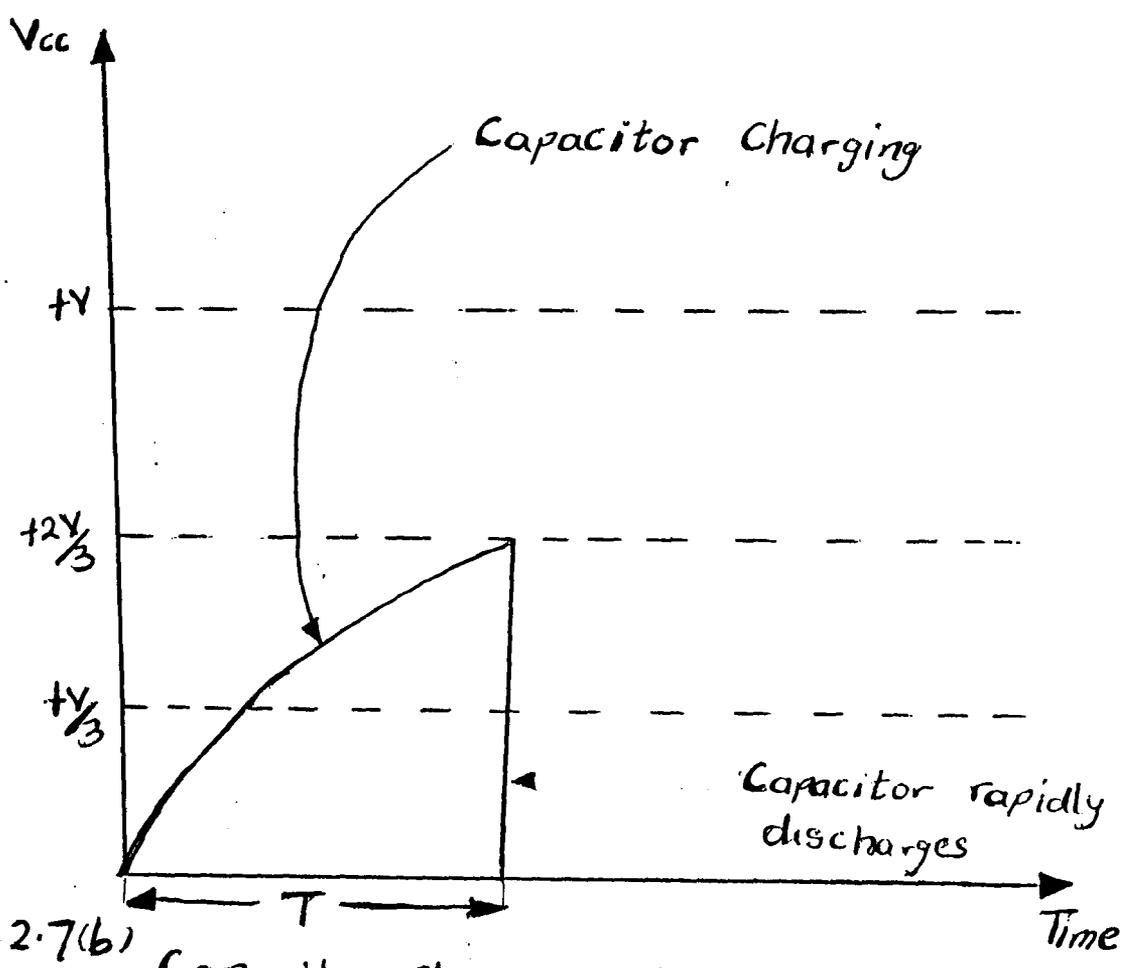


Fig. 2.7(b) Capacitor charge and discharge curve for 555 monostable function.

2.5(B) RELAY AND THE DRIVER

The output from the 555 timer (IC_3) is not enough to switch the relay, hence the incorporation of the transistor switch

$$I_b = I_c(\text{sat})/h_{FE}(\text{sat})$$

I_b is the current just needed to switch on the transistor

To improve the switching time and to ensure that the circuit is driven hard into saturation, an overdrive factor, γ , is normally introduced, so that I_b is given by

$$I_b = \gamma I_c(\text{sat})/h_{FE}(\text{sat})$$

For sufficient overdrive, γ lies between 2 and 5

$$R_b = (V_{bb} - V_{be})/I_b$$

For Tr_3 , (D400) the parameters are:

$$I_c(\text{max}) = 1 \text{ ampere}$$

$$h_{FE} = 120$$

For the relay of 12V, 400 Ω ,

$$\text{Relay current, } I_r = 12/400$$

$$\text{Therefore } I_r = 0.03\text{A}$$

for effective making of the relay,

$$I_c = I_r \text{ (i.e collector current = relay current)}$$

$$\gamma I_c = I_b/h_{FE}$$

$$I_b = \gamma I_c/h_{FE}$$

$$I_b = 3(0.03)/120 (\gamma = 3)$$

$$\text{Therefore, } I_b = 0.75 \text{ mA}$$

V_{bb} = voltage (output) from the 555 timer

$$V_{bb} = 7\text{V}$$

$$V_b = (V_{bb} - V_{be})/I_b$$

$$= 7 - 0.6/(0.75 \times 10^{-3})$$

$$= 8.5 \text{ K}$$

preferred value of $R_b = 10k\Omega$,

power rating of R_b is $= (0.75 \times 10^{-3})^2 \times 10000 \approx \frac{1}{4} \text{ w}$

preferred value of $R_{17} = 10k\Omega$, $\frac{1}{4} \text{ w}$.

Refer to fig. 2.8

D9 is a freewheeling diode (overswing limiting diode). When inductive loads are switched off by semi conductor devices, the inductance sweeps the collector/emitter (or anode/cathode) voltage to a greater value than the supply. This could cause avalanche breakdown which eventually leads to the failure of the device. It is usual therefore in such circuits where this problem is likely to arise to include an overswing limiting diode (Fig 2.8).

Current rating of D9 > load current

Breakdown voltage > $12\sqrt{2}$

Current rating of D9 = 2A (>1.3A)

Voltage rating of D9 = 100V (> $12\sqrt{2}$)

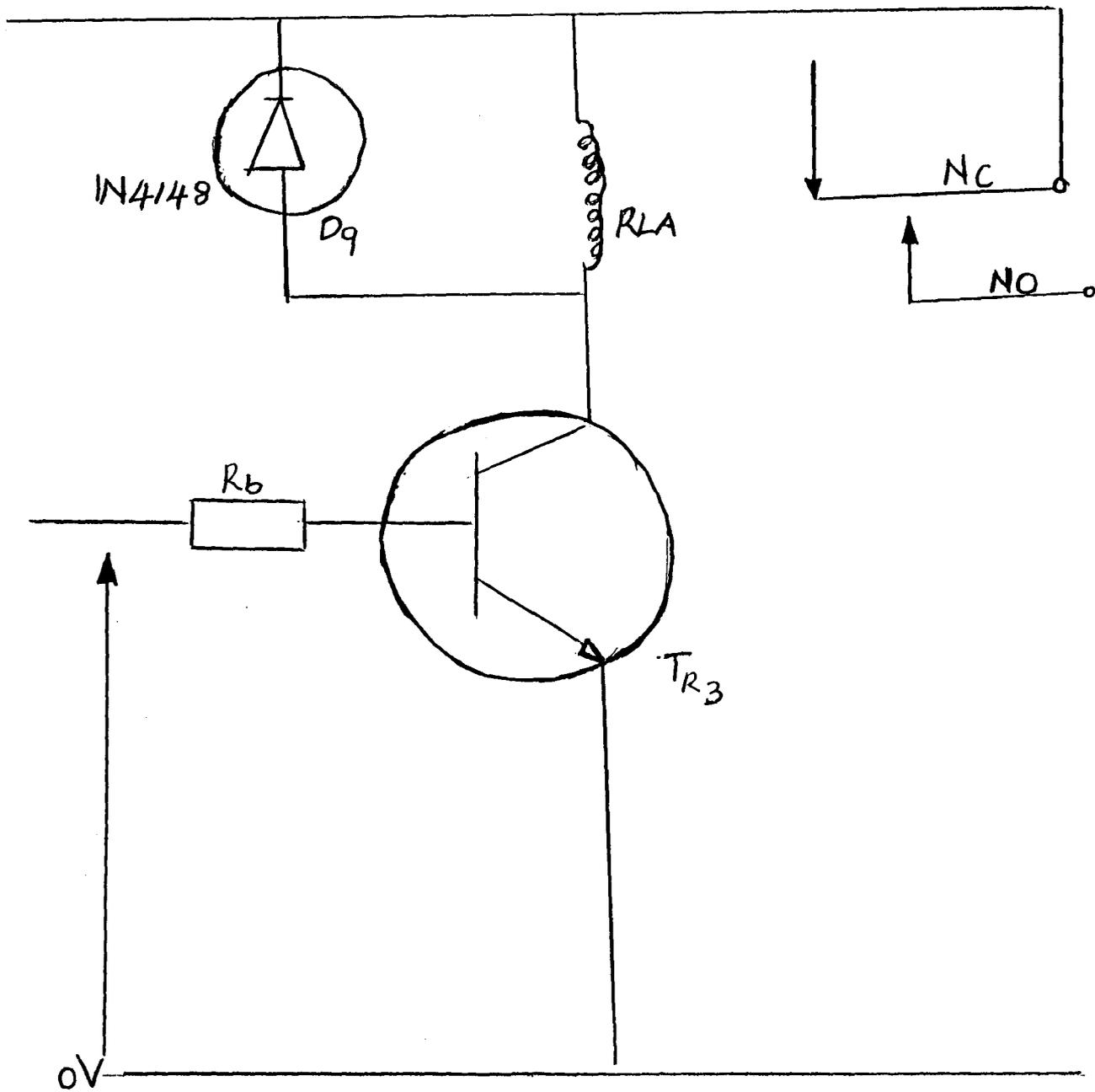


Fig 2.8

Transistor switch.

2.6 ALARM CIRCUIT

The circuit bonded by ABCDE is the circuit (fig 2.9(a)) is a unijunction oscillator. It modulates the basic oscillator circuit. The UJT figure 2.9(b) adds and subtracts base current from Tr4. The current modulation causes the frequency of the multivibrator to shift from a low to a high frequency then turns off momentarily and repeats the cycle over and over again.

The output of the multivibrator Figure 2.9c is direct –coupled through R24 to the base of the power transistor Tr6. The current pulse at the base of Tr6 is sufficient to drive the collector to saturation, thereby connecting the supply voltage across the speaker.

The frequency of the UJT is given by

$$f \approx 1/(RT CT \ln (1/1-\eta)) \text{ HZ}$$

$$\text{Where } \eta = R1/(R1 + R2) V_{cc}$$

For symmetry of pulse, $R1 = R2$

$$\eta = R1/2R1$$

$$= 1/2$$

$$\text{therefore } f = 1/(RT CT \ln 2)$$

with $CT = 4.7\mu\text{f}$, $25V$

$$f \propto 1/RT CT$$

with $RT = 33K$

$$f = 0.1075 \text{ Hz}$$

Refer to fig 10(b)

Transistor Tr4 and Tr5 perform as a free – running multivibrator otherwise called an astable multivibrator.

$$I_c(\text{sat}) = P\{V_{cc} - V_{ce}(\text{sat})\}/R_c$$

$$R_c = \{V_{cc} - V_{ce}(\text{sat})\}/I_c(\text{sat})$$

Tr 4 = Tr5 = 2N3638 PNP transistor

$$h_{FE} = 180$$

For switching circuit, the transistors must operate in the saturation region (i.e fully ON) required value of base current, I_b is given by

$I_b = I_c (\text{Unloaded}) + I_{\text{fan-out current}}$.

Fan – out current is the current that must be available to drive external loads.

Choosing an over drive factor of 3 to ensure that the current is driven hard into saturation

$$I_b = I_c (\text{sat})/h_{FE}(\text{sat})$$

$$I_c(\text{sat}) = 30 \text{ mA}$$

$$= 3(30 \times 10^{-3})/180$$

$$\text{therefore } I_b = 0.5 \text{ mA}$$

from the circuit fig 10, the loop equation gives:

$$V_{cc} = V_{be} + I_b R_b$$

$$I_b R_b = V_{cc} - V_{be}$$

$$R_b = (12 - 0.6)/(0.5 \times 10^{-3})$$

$$\text{Therefore } R_b = 22.8 \text{ K}$$

$$\text{Therefore } R_b = 22 \text{ K}$$

The frequency of operation is given by

$$1/f = (C_1 R_1 + C_2 R_2) \ln 2$$

$$f = 1/\{0.693(C_1 R_1 + C_2 R_2)\}$$

$$= 1/(0.7C_1 R_1 + C_2 R_2)$$

$$f = 1/(t_1 + t_2) \text{ Hz} \quad t_1 = 0.7C_1 R_1$$

$$t_2 = 0.7C_2 R_2$$

If $R_1 C_1 = R_2 C_2$, then

$$f = 1/1.4R_1 C_1 \text{ or } 1/1.4R_2 C_2$$

$$C_1 = 1/1.4R_1 f$$

$$R_1 = R_2 = R_b$$

$$C_1 = C_2 = C$$

$$C = 1/1.4R_b f$$

$$f = 1/1.4C R_b$$

$$f \propto 1/CR_b$$

Fixing the value of C at $0.1 \mu\text{F}$

$f \propto 1/R_b$

$$C = 0.1 \times 10^{-6} \text{F}$$

$$R_b = 22\text{k}$$

$$\text{Therefore } f = 1/(1.4 \times 0.1 \times 10^{-6} \times 22 \times 10^3)$$

$$\text{Therefore } f = 324.5.68.\text{Hz}$$

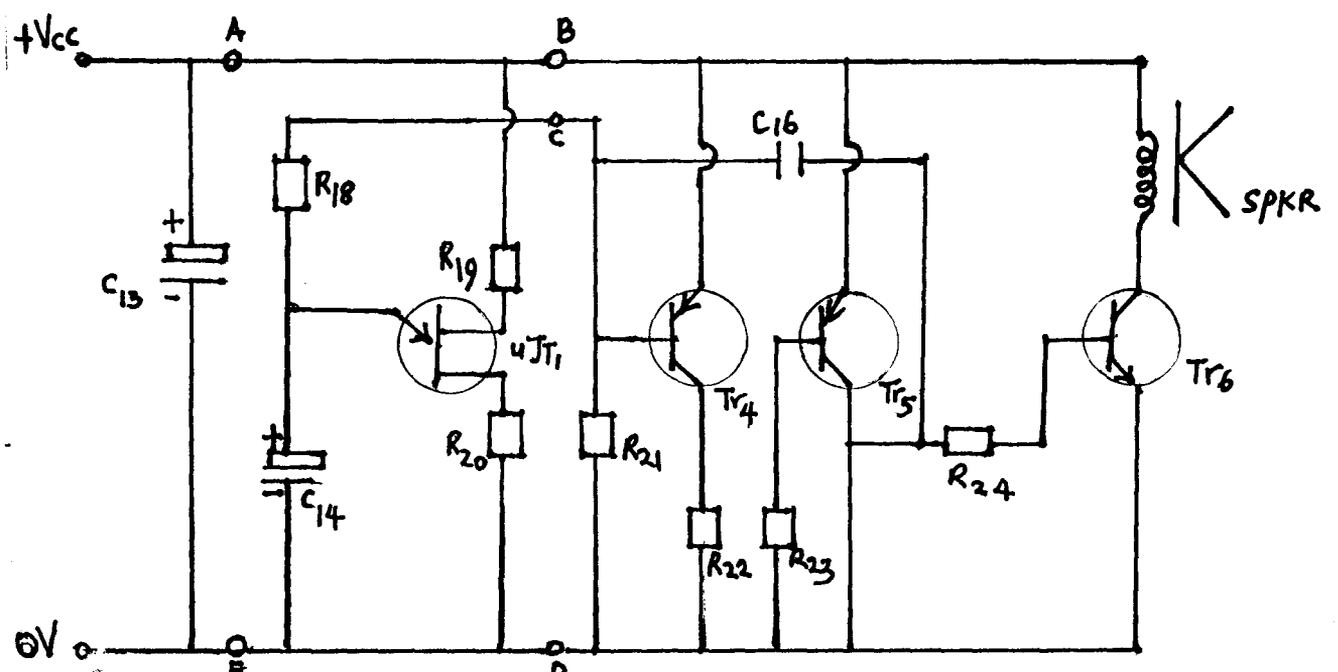


FIG 29(a) THE COMPLETE CIRCUIT OF ALARM SOUNDER.

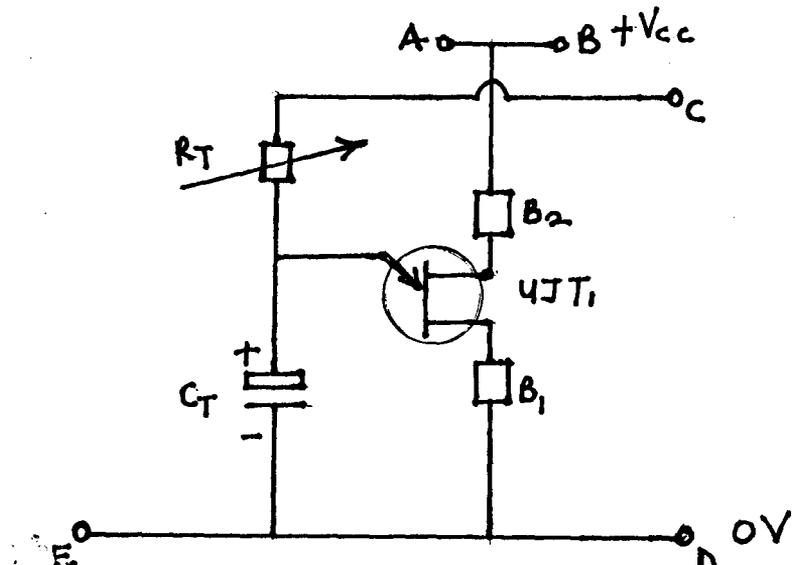


FIG 29(b) UNIJUNCTION OSCILLATOR

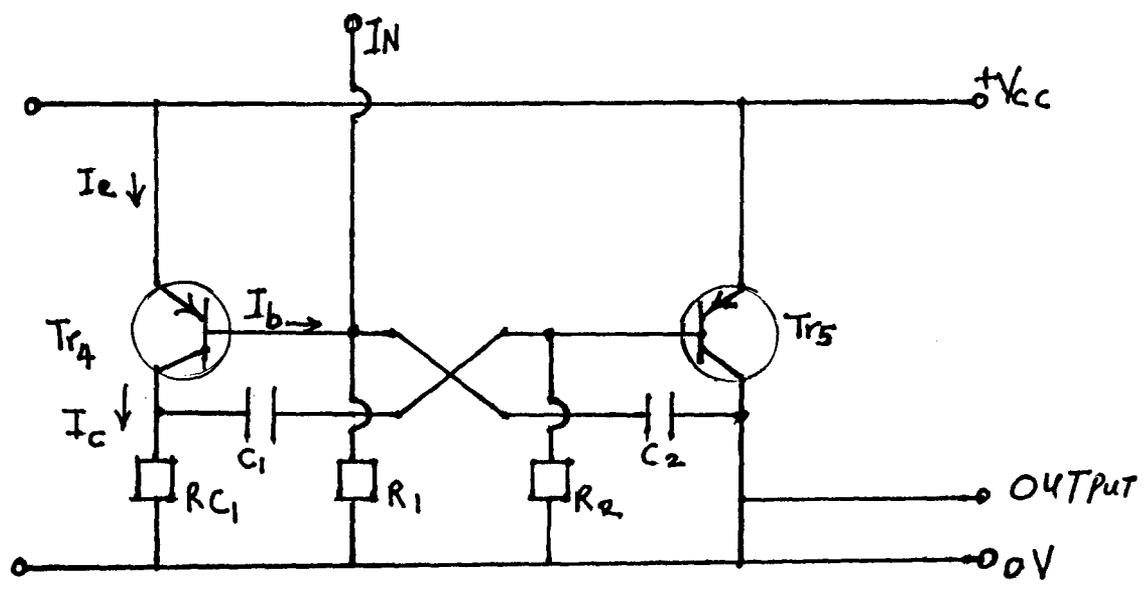


FIG 29(c) ASTABLE MULTIVIBRATOR.

CHAPTER THREE

CONSTRUCTION; TESTING AND RESULTS

3.1 CONSTRUCTION

The project was constructed based on the design. Each of the stages was constructed and tested separately before coupling.

The components stated in the design were used and the methods were followed

3.2 TESTING OF THE ALARM CIRCUIT

Values of frequencies were obtained for corresponding values of the base resistor, R_b

TABLE 1.1 Result of alarm circuit

Base Resistor, R_b (K)	Frequency Of Operation (In Hz)
10	700
15	475
20	350
25	280
30	230
35	200

The value of capacitance, C was fixed at $0.1 \mu\text{F}$

Frequency of operation is inversely proportional to the base resistor, R_b

$$f = 1/1.4 \times C \times R_b$$

3.3 TESTING OF TIMER CIRCUIT

Various values of time in seconds were obtained for corresponding values of the timing resistor with the value of capacitor fixed at $C = 10\mu F$

Table 1.2 of result for timer circuit

Timing Resistor (K)	Time Of Operation (Seconds)
100	1
150	1.5
200	2
250	2.5
300	3
350	3.5
400	4

$t \propto R$ (i.e time of operation is directly proportional to the timing resistor)

$$t = 1.1. CR$$

3.4 TESTING OF THE INFRARED ALARM SYSTEM

The circuit was tested in broad day light and the performance was satisfactory.

It was also discovered that total darkness does not favour the performance of the circuit

Table 1.3 Sensitivity of the infrared alarm system

Nature of object	range
Large object	150 – 300mm
Small object	25 mm
Very small object	Not detected
Large and highly reflective object	150 – 500mm

3.5 LIST OF COMPONENTS USED

RESISTORS (1/4w)

R1	=	470 Ω
R2	=	56 Ω
R3	=	20 Ω
R4	=	10 Ω (variable)
R5	=	68 Ω
R6	=	12 Ω
R7	=	30 Ω
R8	=	68 Ω
R9	=	30 Ω
R10	=	68 Ω
R11	=	12 Ω
R12	=	39 Ω
R13	=	47 Ω (variable)
R14	=	1M Ω
R15	=	10 Ω
R16	=	470 Ω
R17	=	10 Ω
R18	=	33 Ω
R19	=	100 Ω
R20	=	100 Ω
R21	=	22 Ω
R22	=	22 Ω
R23	=	22 Ω
R24	=	47 Ω

TRANSISTORS

Tr1	=	BC1096
Tr2	=	BC1096
Tr3	=	D400
Tr4	=	2N3638
Tr5	=	MJE3055
Tr6	=	2N3638
UJT	=	2N2646

DIODES

D1	=	IN5391
D2	=	IN5391
D3	=	12V Zener diode
D4	=	TIL 38
D5	=	TIL 100
D6	=	OA 91
D7	=	OA 91
D8	=	IN 4148
D9	=	IN 4148

CAPACITORS

C₁	=	2200μF ELECTROLYTIC, 25v	<u>REPLAYS</u>
C₂	=	2.2μF 50v Electrolytic	
C₃	=	10nF ceramic	RLA1 = 6V, 4088 Ω
C₄	=	2.2μF, 50v electrolytic	RLA2 = 12V, 400 Ω
C₅	=	1μF ceramic	
C₆	=	1μF ceramic	
C₇	=	22pF ceramic	
C₈	=	1μF	
C₉	=	1μF	
C₁₀	=	100μF, 16v electrolytic	
C₁₁	=	10nF	
C₁₂	=	10μF	
C₁₃	=	50μF, 25v electrolytic	
C₁₄	=	4.7μF, 25v electrolytic	
C₁₅	=	0.1μF ceramic	
C₁₆	=	0.1μF ceramic.	

ICS

IC₁	=	555Timer
IC₂	=	CA3140E
IC₃	=	555Timer
S1	=	Toggle
F1	=	Fuse 1 A
T1	=	240/12v center tapped transformer
SPKR	=	speaker, 15W, 8Ω

CHAPTER FOUR

CONCLUSION AND RECOMMENDATION

4.1 CONCLUSION

Form the table of results for alarm circuit, it could be seen that the frequency of operation of the alarm circuit, $f \propto 1/Rb$.

Also, from the table of result for the timer circuit, timing operation, $t \propto R$, where R is the value of timing resistor. That is the time of operation is directly proportional to the timing resistor.

From the general outcome of the testing of the infrared alarm system, it could be seen that the amount of the infrared energy emitted depends on the nature of the body and the wavelength of the radiation. This agrees with planck's law.

Planck's Law:

$$W(\lambda) = C_1 \lambda^{-5} C_2 \lambda^{-5} (e^{c_2/\lambda T} - 1)^{-1} \text{W / cm}^2 / \mu\text{m}$$

$W(\lambda)$ =energy of radiation it shows that the Electron emerge with a kinetic energy increases directly with the frequency of the light

$$\text{Where } C_1 = 2 \pi h c^2 = 3.7415 \times 10^4 \text{ W. } (\mu\text{m})^4 / \text{cm}^2$$

$h = 6.6256 \times 10^{-34} \text{ W. s}^2$ is Planck's constant

$c = 3 \times 10^{10} \text{ cm/s}$ is the velocity of light in vacuum

λ = wavelength in micrometers

T = absolute temperature in degree Kelvin

Therefore, the objectives of the project were achieved.

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